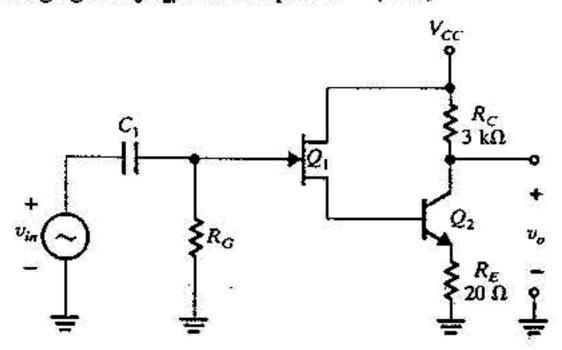
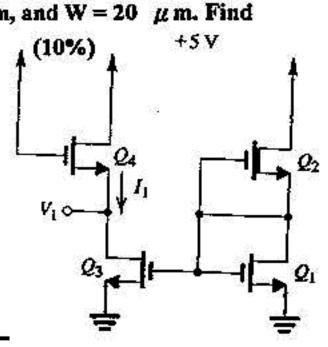
- 1. A 6.8 V Zener diode specified at 5 mA to have $V_z = 6.8$ V and $r_z = 20$ Ω with $I_{ZK} = 0.2$ mA, is operated in a regulator circuit using a 200 Ω resistor and a 9 V supply.
 - (a) Estimate the knee voltage of the Zener. (5%)
 - (b) For no load, what is the lowest supply voltage for which the Zener remains in breakdown operation? (5%)
 - (c) For the nominal supply voltage, what is the maximum load current for which the Zener remains in breakdown operation? (5%)
- 2. The two-transistor amplifier shown in the following Figure combines an FET and a BJT to achieve both a high input impedance and a large voltage gain. By considering the g_ω of Q₁ to be 1 mS (or 1 mA/V), and r_x and β for Q₂ to be 1 kΩ and 100, respectively, determine the voltage gain v_o/v_{in} of the amplifier. (10%)



3. For the devices in the circuit of the following Figure, $|V_t| = 1 \text{ V}$, $\lambda = 0$, $\gamma = 0$, $\mu_a C_{ex} = 20 \mu \text{ A/V}^2$, $L = 1 \mu \text{ m}$, and $W = 20 \mu \text{ m}$. Find the labeled current (L) and voltage (V₁). (10%)

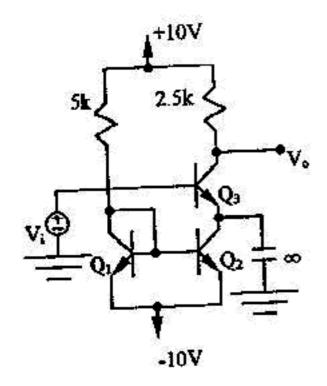


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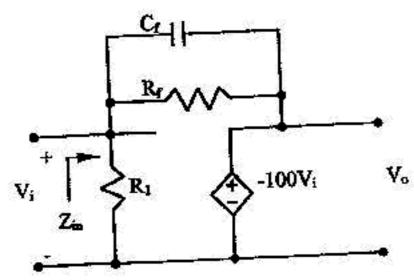
- 4. In the circuit, the BJTs have $\beta=100$ and $r_*=\infty$.
 - (1) Determine the dc voltages V_{c2} and V_{c3} .
 - (2) Find the voltage gain V/Vi-

(10%)



 An amplifier can be modeled by the equivalent circuit as shown. Find the input impedance Z_{in} and the pole of V/V_i.

(10%)



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- The rated junction temperature of a power BJT is T_{junce}=150 °C. The thermal resistance for the BJT package are θ_{dov-case}=2 °C/W and θ_{case-case}=18 °C/W. It is operated to dissipate a power of 5W.
 - (1) Find the maximum allowable ambient temperature T_{A,max} when it is operated without heat sink.
 - (2) If the BJT is attached to a heat sink which gives $\theta_{constant}$ =4 *C/W and $\theta_{constant}$ =6 *C/W, find $T_{A,max}$.

(10%)

- 7. (a) Write the 2nd order band-pass filter function T(S)=? 5%
- (b) For the same band-pass filter function T(S), if $|T(\omega_a)|=|T(\omega_b)|$ and $\omega_a\neq\omega_b$, find $\omega_a\bullet\omega_b=?.$ (15%)
- 8. (20%) Draw a decoder used for memory with 3 address lines in transistor level. Also show the logic function of each output.